

0 1 2 3 4 5 6 7 8 9
 10 11 12 13 14 15 16 17 18 19
 20 21 22 23 24 25 26 27 28 29
 30 31 32 33 34 35 36 37 38 39
 40 41 42 43 44 45 46 47 48 49
 50 51 52 53 54 55 56 57 58 59
 60 61 62 63 64 65 66 67 68 69
 70 71 72 73 74 75 76 77 78 79
 80 81 82 83 84 85 86 87 88 89
 90 91 92 93 94 95 96 97 98 99

Buffering

2. A system as claimed in Claim 1, wherein each table identifies, for its respective device, signal processing capabilities for that device, together with the latency associated with each such capability.

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4. A system as claimed in Claim 3, wherein said source device further comprises multiplexing means coupled with said means arranged to apply a delay and arranged to combine said at least two streams into a single data stream for transmission to said destination devices.

5. A system as claimed in Claim 2, wherein one or more table entries

is in the form of an algorithm requiring data from the device reading the table to enable determination of the latency of the device holding said table.

6. A system as claimed in Claim 5, wherein the determination on the
5 basis of the algorithm is implemented by the device reading the table, said device having downloaded the algorithm from the device holding the table.

7. A system as claimed in Claim 5, wherein the determination on the
basis of the algorithm is implemented by the device holding the table, the results
10 of the implementation being transmitted via said interconnection to the device reading the table.

8. A system as claimed in Claim 1, wherein all destination devices
maintain a respective table.

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9. A system as claimed in Claim 1, wherein said means arranged to
apply a delay comprises buffering means.

10. A system as claimed in Claim 1, wherein said means arranged to
20 apply a delay comprises means arranged to selectively apply a delay to reading of one or each of said data streams from a source thereof.

11. Data processing apparatus comprising the technical features of a
source device in a system as claimed in Claim 3.

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12. Data processing apparatus as claimed in Claim 11, further
comprising the technical features of a destination device in a system as claimed
in any of Claims 1 to 10.

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